Register Number………………………

M.E., DEGREE EXAMINATIONS: NOV/DEC 2012

Third Semester

EMBEDDED SYSTEMS

EST552: Software Technology for Embedded Systems

Time: Three Hours   **Maximum Marks: 100**

Answer All Questions:-

PART A (10 x 2 = 20 Marks)

1. What are the advantages of ROM Emulator?

2. What are three factors make writes difficulty in Flash memory?

3. What is Embedded Operating System?

4. List out the real time characteristics

5. What is Interrupt Request ?

6. What is Function-Queue-Scheduling Architecture?

7. How does the scheduler know when a task has become blocked or unblocked in RTOS?

8. What is Preemptive RTOS?

9. What is Storage Scope?

10. What is Logic Analyzer?

PART B (5 x16=80) Marks

11. a) Explain the Embedded Software development process with suitable diagram

(OR)

    b) (i) Discuss the Remote debuggers in detail.   (10)

       (ii) Explain the role of infinite loop.   (6)

12. a) Describe the Zilog 85230 serial controller in detail.

(OR)

b) Explain the increasing code efficiency and decreasing code size in detail.

13. a) Explain the architecture of a system with DMA in detail

(OR)

b) Discuss   the Build-Ins on the Microprocessor.

14. a) Explain in detail RTOS Semaphores

(OR)

b) Illustrate the Interrupt in an RTOS environment.

15. a) Describe the Instruction set simulators.

(OR)

b) Illustrate the tool chain for building embedded software with suitable diagram.

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