

L 1059

B.E./B.Tech. DEGREE EXAMINATION, MAY/JUNE 2006.

Fourth Semester

Information Technology

CS 1304 — MICROPROCESSORS AND MICROCONTROLLERS

(Regulation 2004)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — ($10 \times 2 = 20$ marks)

1. What is the use of ALE signal in 8085?
2. Give the format of flag register in 8085.
3. What are the different types of interrupts in 8086?
4. State the interrupt priorities of 8086.
5. What are the two operating modes of 8086?
6. State the function of Watch dog timer.
7. List the features of 8051.
8. List the different modes of 8255 for I/O operation.
9. State the function of $\overline{DSR}$ and $\overline{DTR}$ pins in 8251.
10. List the features of 8259.

PART B — ($5 \times 16 = 80$ marks)

11. (i) Explain the architecture of 8085 with neat diagram. (8)
(ii) Explain the pin definition of 8085. (8)
12. (a) Explain the Addressing modes of 8086 with examples.

Or

- (b) Describe the procedures and macros of 8086.

13. (a) With a neat sketch describe the minimum and maximum mode operation of 8086.

Or

- (b) Describe the system design using 8086.

14. (a) With neat block diagram explain the DMA controller.

Or

- (b) With block diagram explain Interrupt controller.

15. (a) Explain the operational features of 8051.

Or

- (b) Describe the memory and I/O addressing of 8051.

Time : Thr

1. Wri
2. List
3. Wr
4. Co
5. W
6. V
7. I
- 8.
- 9.
- 10.
- 11.