

**B.E DEGREE EXAMINATIONS: NOV/DEC 2014**

(Regulation 2009)

Seventh Semester

**ELECTRICAL AND ELECTRONICS ENGINEERING**

EEE118: Power System Analysis & Stability

**Time: Three Hours**

**Maximum Marks: 100**

**Answer all the Questions:-**

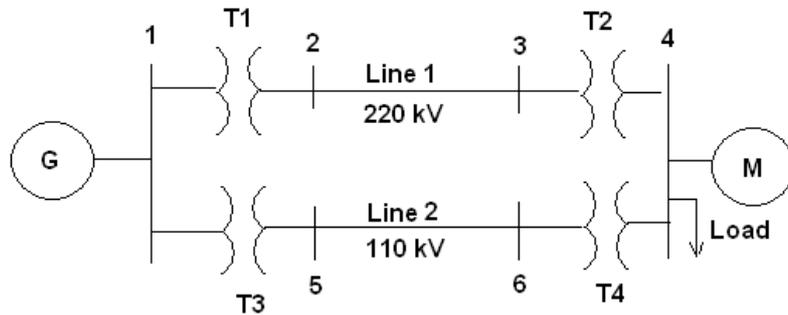
**PART A (10 x 1 = 10 Marks)**

1. Which of the following assumptions made is not true with regard to the reactance diagram?
  - a) Resistances are neglected
  - b) Neutral reactances are neglected
  - c) Capacitance of transmission line are neglected
  - d) Static loads and induction motors are included
2. A Y connected generator rated at 300MVA, 33 kV has a reactance of 1.24 pu. Find the actual value of reactance
  - a) 4.5  $\Omega$
  - b) 5.4  $\Omega$
  - c) 2.9  $\Omega$
  - d) 9.2  $\Omega$
3. Quantities specified in load bus are
  - a) P and |V|
  - b) P and Q
  - c) |V| and  $\delta$
  - d) Q and |V|
4. Which is not true with regard to NR method?
  - a) faster
  - b) accurate
  - c) Memory requirement is more
  - d) More iterations for convergence
5. Which of the following fault occurs more frequently?
  - a) Three phase fault
  - b) Single line to ground fault
  - c) Double line to ground fault
  - d) Line to line fault
6. Which of the following is not a shunt type fault?
  - a) Three phase fault
  - b) One open conductor fault
  - c) Single line to ground fault
  - d) Line to line fault
7. Which is the formula for surge impedance?



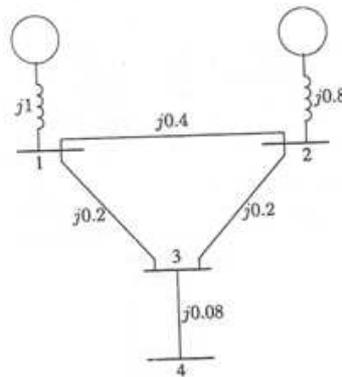
impedance diagram with all impedances marked in pu. The data for each device is given below.

- G: 90 MVA 22 kV X = 18%
- T<sub>1</sub>: 50 MVA 22 / 220 kV X = 10%
- T<sub>2</sub>: 40 MVA 220 / 11 kV X = 6%
- T<sub>3</sub>: 40 MVA 22 / 110 kV X = 6.4%
- T<sub>4</sub>: 40 MVA 110 / 11 kV X = 8%
- M: 66.5 MVA 10.45 kV X = 18.5%



The three phase load at bus 4 absorbs 57 MVA, 0.6 power factor lagging at 10.45 kV. Line 1 and line 2 have reactances of 48.4 and 65.43 Ω respectively.

22. a) (i) List the quantities specified and quantities to be determined for various types of buses. (4)
- (ii) Find the bus admittance matrix of the following network.



(10)

(OR)

- b) Explain the algorithmic steps for getting power flow solutions using Gauss Seidal method.

23. a) Derive the symmetrical fault analysis of the system using bus admittance matrix.

(OR)

- b) Derive the fault current of a system when a double line to ground fault occurs.
24. a) Explain the Bewley's Lattice diagram with neat sketches.  
(OR)  
b) Derive an expression for standing wave equation.
25. a) Define transient stability. With proper derivation explain how the transient stability is analysed using equal area criterion.  
(OR)  
b) Give an expression for swing equation. Solve the swing equation by Euler's method.

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