



B.TECH DEGREE EXAMINATIONS: MAY 2015

(Regulation 2009)

Third Semester

INFORMATION TECHNOLOGY

CSE106: System Software

Time: Three Hours

Maximum Marks: 100

Answer all the Questions:-

PART A (10 x 1 = 10 Marks)

1. If the exponent has the value e and fraction has value f , the floating point number in SIC is represented as
 - a) $f * 2^{(e - 1024)}$
 - b) $f * (e - 1024)$
 - c) $f * 10^e$
 - d) $f * 2^e$
2. Match
 - a) register indirect with index
 - b) PC relative
 - c) immediate
 - d) indirect
 - i) $TA = (PC) + disp$
 - ii) LDA #9
 - iii) J @RETADDR
 - iv) $TA = (Reg_1) + (Reg_2)$
 - a) a-i,b-ii,c-iii,d-iv
 - b) **a-iv,b-i,c-ii,d-iii**
 - c) a-ii,b-i,c-iv,d-iii
 - d) a-iv,b-iii,c-ii,d-i
3. Match activities to the passes
 - a. Object code generation.
 - b. Literals added to literal table.
 - c. Listing printed.
 - d. Address resolution of local symbols.
 - i) pass 1
 - ii) pass 2
 - a) a-i,b-ii,c-i,d-ii
 - b) **a-ii,b-i,c-ii,d-i**
 - c) a-ii,b-ii,c-i,d-i
 - d) a-ii,b-i,c-i,d-ii
4. _____ is the process of modifying the addresses used in the address sensitive instructions of the program such that the program can execute correctly from a designated area of the memory.
 - a) loading
 - b) linking
 - c) relocation
 - d) execution
5. _____ performs the linking function, produces information about allocation and writes this information along with the program code in a file
 - a) linking loader
 - b) dynamic loader

ii) Write SIC code to add two arrays ALPHA and BETA and store in array GAMMA (5)

(OR)

b) i) Explain in detail UltraSPARC architecture with suitable examples (9)

ii) Compare and contrast RISC and CISC architectures (5)

22. a) i) Explain one-pass assembler functions with example program (9)

ii) Explain the forward reference problem in one pass assembler. (5)

(OR)

b) i) List the features of the assemblers. (5)

ii) Discuss in detail about MASM assembler. (9)

23. a) Explain algorithm and data structures used for a linking loader in detail.

(OR)

b) i) Explain dynamic linking in detail. (7)

ii) Explain machine independent loader features in detail. (7)

24. a) Explain in detail macro processor algorithm and data structures

(OR)

b) Explain about conditional macros with examples

25. a) Explain in detail the structure of the compiler. Brief on the functionality of each phase

(OR)

b) Explain in detail the structure of an editor with a figure
