



**M.E DEGREE EXAMINATIONS: JUNE 2015**

(Regulation 2014)

Second Semester

**EMBEDDED SYSTEM TECHNOLOGIES**

P14EST202: Embedded Control Systems

**Time: Three Hours**

**Maximum Marks: 100**

**Answer all the Questions:-**

**PART A (10 x 1 = 10 Marks)**

1. Match the following

[K<sub>2</sub>]

Memory size	No of address lines required
A. 1M x 4	1. 16
B. 1K by 8	2. 10
C. 64K x 32	3. 22
D. 4M x 16	4. 20

	A	B	C	D
a)	1	2	3	4
b)	2	4	3	1
c)	2	1	4	3
d)	4	2	1	3

2. An embedded system must have

[K<sub>1</sub>]

- a) processor
- b) Processor and IO lines
- c) Processor and memory
- d) Hard disk

3. The most efficient display system

[K<sub>2</sub>]

- a) LCD
- b) LED
- c) CRT
- d) 7-segment

4. Hardware interrupts of 8085 microprocessor is

[K<sub>1</sub>]

- a) RST0
- b) INTR
- c) TRAP
- d) Both b and c

5. What is the major advantage of the R/2R ladder digital-to-analog (DAC) as compared to a binary-weighted digital-to-analog DAC converter? [K<sub>2</sub>]
- (i) It only uses two different resistor values.
  - (ii) Does not require larger resistor value
  - (iii) Resistor selection is difficult
  - (iv) Faster conversion
- Which are all the statements are correct?
- a) i, ii correct
  - b) i, iii correct
  - c) iii, iv correct
  - d) ii, iv are correct.
6. Device that automatically detecting the port [K<sub>2</sub>]
- a) USB connected devices
  - b) Serial port connected devices
  - c) Parallel port connected devices
  - d) Both b and c
7. Number of nodes in RS485 interface is \_\_\_\_\_ [K<sub>2</sub>]
- a) 32
  - b) 23
  - c) 11
  - d) 28
8. Assertion (A): Telephony is the technology associated with the electronic transmission of voice [K<sub>3</sub>]
- Reason (R) : Internet telephony uses the Internet
- a) A is false but R is true
  - b) A is true but R is false.
  - c) Both **A** and **R** are correct and **R** is the correct explanation of **A**
  - d) Both **A** and **R** are correct and **R** is the not correct explanation of **A**
9. Rs232 DB connector has \_\_\_\_\_ pins [K<sub>1</sub>]
- a) 8
  - b) 9
  - c) 10
  - d) 12
10. H-bridge operation : to change the direction of rotation of DC motor. [K<sub>3</sub>]
- i) supply is given to H-bridge
  - ii) switching sequence program is loaded in the microcontroller
  - iii) gate pulse to the switches are given by microcontroller
  - iv) motor can be rotated in required direction
- Find the correct sequence?
- a) iii, i, ii, iv
  - b) ii, iii, i, iv
  - c) ii, i, iii, iv
  - d) ii, i, iii, iv

**PART B (10 x 2 = 20 Marks)**

11. Define Embedded system. [K<sub>1</sub>]
12. State Bit masking. [K<sub>1</sub>]
13. What is the use of keyinit ( ) code? [K<sub>2</sub>]
14. What is keyboard debouncing? [K<sub>1</sub>]
15. Draw interrupt vector table. [K<sub>1</sub>]
16. State port off set. [K<sub>1</sub>]
17. List the demerits of RS 232 interface. [K<sub>1</sub>]
18. How many drivers and receivers RS485 has? [K<sub>2</sub>]
19. Define multiclouser problem. [K<sub>1</sub>]
20. Draw the schematic of H-bridge. [K<sub>1</sub>]

**PART C (10 x 5 = 50 Marks)**

21. Compare data lines and Address Lines. [K<sub>2</sub>]
22. With neat sketch explain IC 742 LS 244. [K<sub>2</sub>]
23. Draw and explain the Matrix key board driver flow diagram. [K<sub>2</sub>]
24. Write short notes on Auto repeat of matrix key board scanning algorithm. [K<sub>1</sub>]
25. Write short notes on interrupts and its importance. [K<sub>1</sub>]
26. Discuss the R-2R ladder Digital to Analog converter. [K<sub>2</sub>]
27. Compare synchronous and asynchronous communication. [K<sub>2</sub>]
28. With neat sketch explain serial buffered I/O. [K<sub>1</sub>]
29. Write short notes on H-Bridge. [K<sub>2</sub>]
30. Discuss the importance of Embedded systems in telephonic systems. [K<sub>1</sub>]

**PART D (2 x 10 = 20 Marks)**

31. Explain with a real time example how hardware of an embedded system controlled by software. [K<sub>3</sub>]
32. Write an Embedded C program to change the direction of rotation of DC motor using H-bridge with necessary diagram. [K<sub>3</sub>]

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