



**B.E DEGREE EXAMINATIONS: MAY 2015**

(Regulation 2013)

Fourth Semester

**COMPUTER SCIENCE AND ENGINEERING**

U13CST403: Microprocessors

**Time: Three Hours**

**Maximum Marks: 100**

**Answer all the Questions:-**

**PART A (10 x 1 = 10 Marks)**

1. Accumulator based microprocessor example is:
  - a) Intel 8085
  - b) Motorola 6809
  - c) A and B
  - d) None of these
2. The external system bus architecture is created from ----- architecture
3. Direction flag is used with
  - a) String instructions.
  - b) Stack instructions.
  - c) Arithmetic instructions.
  - d) Branch instructions.
4. Give one way which a microprocessor can come out of Halt-----
5. What will be the contents of register AL after the following has been executed  
MOV BL, 8C  
MOV AL, 7E  
ADD AL, BL
  - a) 0A and carry flag is set
  - b) 0A and carry flag is reset
  - c) 6A and carry flag is set
  - d) 6A and carry flag is reset
6. \_\_\_\_\_ Interrupt has the highest priority
7. 8251 is a
  - a) UART
  - b) USART
  - c) Programmable Interrupt controller
  - d) Programmable interval timer/counter
8. The significance of “!” in a load/store instruction is -----
9. Which one of the following ARM processors contains a Snoop Control Unit (SCU), for hardware cache coherency?
  - a) Cortex-A8
  - b) Cortex-M3



25. a) Explain in detail with examples of various Instruction types of ARM Processor.

**(OR)**

b) Explain in detail about ARM Coprocessor instruction

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